
EP9312 USB version 1.1 Slave Schematic

The attached schematic shows a reference design for attaching the USB bus to the SRAM interface.

Contacting Cirrus Logic Support

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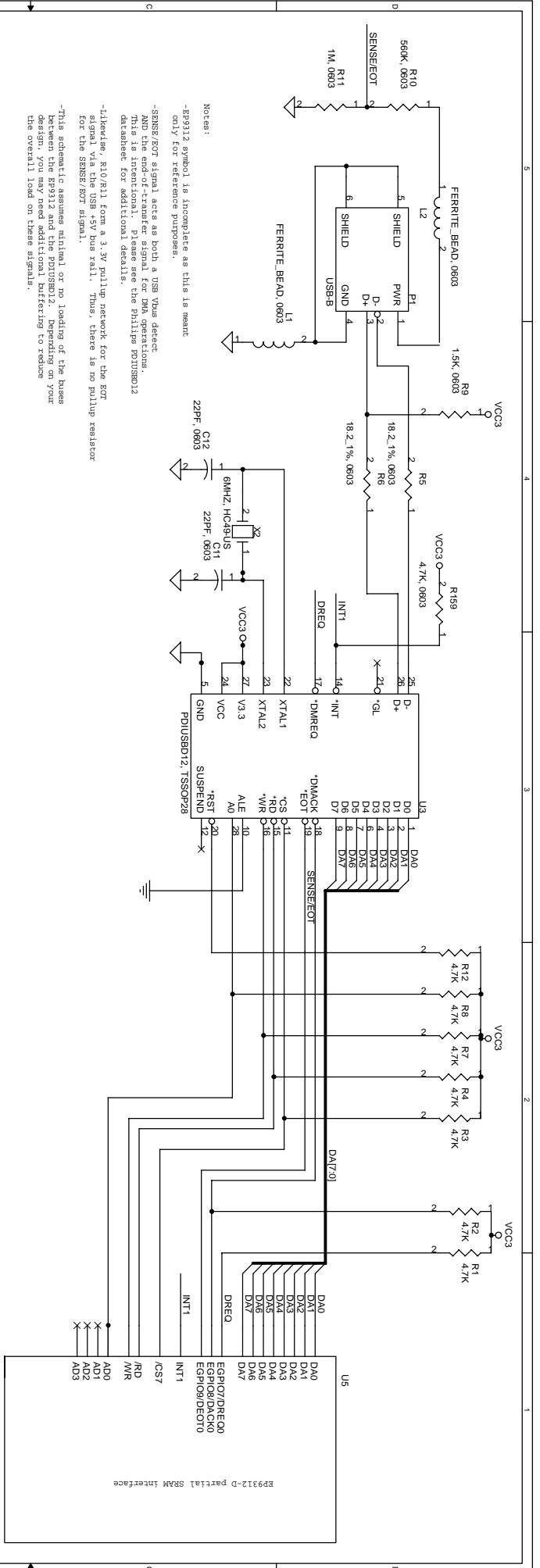
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Notes:
 -pp312 symbol is incomplete as this is meant only for reference purposes.
 -SENSE/EOT signal acts as both a USB Vbus detect AND the end-of-character signal for DMA operations. Please refer to the Philips PP312 USB1.1 datasheet for additional details.
 -Likewise, R10/R11 form a 3.3V pullup network for the EOT signal via the USB +5V bus rail. Thus, there is no pullup resistor for the SENSE/EOT signal.
 -This schematic assumes minimal or no loading of the buses between the PP312 and the PP312. Depending on your design, you may need additional buffering to reduce the overall load on these signals.

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